



6,762,450.

7 CofC

PTO/SB/21 (03-03)

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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	09/876,102
	Filing Date	June 6, 2001
	First Named Inventor	Gurtej Sandhu et al.
	Art Unit	2811
	Examiner Name	S. Crane
Total Number of Pages in This Submission	Attorney Docket Number	MI22-1736

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to a Technology Center (TC)
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<input type="checkbox"/> Response to Missing Parts/Incomplete Application	<b>Remarks</b> Additional Enclosure: Request for Certificate of Correction  Patent No.: 6,762,450 B2 Issued: July 13, 2004	
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

Certificate  
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of Correction

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual	Jennifer J. Taylor, Ph.D., Reg. No. 48,711 Wells St. John, P.S.
Signature	<i>Jennifer J. Taylor</i>
Date	January 11, 2005

CERTIFICATE OF TRANSMISSION/MAILING	
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date: 1/11/2005	
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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Patent No. .... 6,762,450 B2  
Patent Issue Date ..... July 13, 2004  
Application Serial No. .... 09/876,102  
Filing Date ..... June 6, 2001  
Assignee ..... Micron Technology, Inc.  
Inventorship ..... Sandhu et al.  
Attorney's Docket No. .... MI22-1736  
Title: Method of Forming a Capacitor and A Capacitor Construction

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT**  
**FOR PTO MISTAKE (37 C.F.R. 1.322(a))**

To: Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
ATTN: Decision and Certificate of Correction  
Branch of the Patent Issue Division  
  
From: Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)  
Wells St. John P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 6,762,450 B2, granted July 13, 2004, in accordance with the Certificate of Correction form attached hereto in duplicate.

The errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

Since this Certificate of Correction is being requested due to PTO errors, it is believed that no fee is due. However, in the event that a fee is required for issuance of this Certificate of Correction, please charge the fee specified under

37 C.F.R. § 1.20(a) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Attached hereto, in duplicate is Form PTO-1050, with at least one copy being suitable for printing.

Respectfully submitted,

Dated: January 11, 2005

By: Jennifer J. Taylor  
Jennifer J. Taylor, Ph.D.  
Reg. No. 48,711

UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,762,450 B2  
DATED : July 13, 2004  
INVENTOR(S) : Gurtej Sandhu et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page 1, "OTHER PUBLICATIONS", in the Institute of Semiconductor Physics reference, please delete "Temmter, D.," before "Multilayer Vertical" and insert -- Temmler, D.,--.

Title Page 1, "OTHER PUBLICATIONS", in the University of California reference, please delete "Nierdef, H.," before "Organometallic Chemical" and insert --Niemer, H.,--.

Title Page 2, "OTHER PUBLICATIONS", in the IEDM reference, please delete "H. Wannabe et al.," before "Stacked Capacitor" and insert --H. Watanabe et al.,--.

Col. 5, line 56, please delete "is" after "layer 34".

Page  
1 of 1

**Mailing Address of Sender:**

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Patent No. 6,762,450 B2

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